



Title: Programmable I/O Element Circuit for High Speed Logic Devices

Inventors: Nguyen et al.

Application No.: 10/685,355

1 of 7

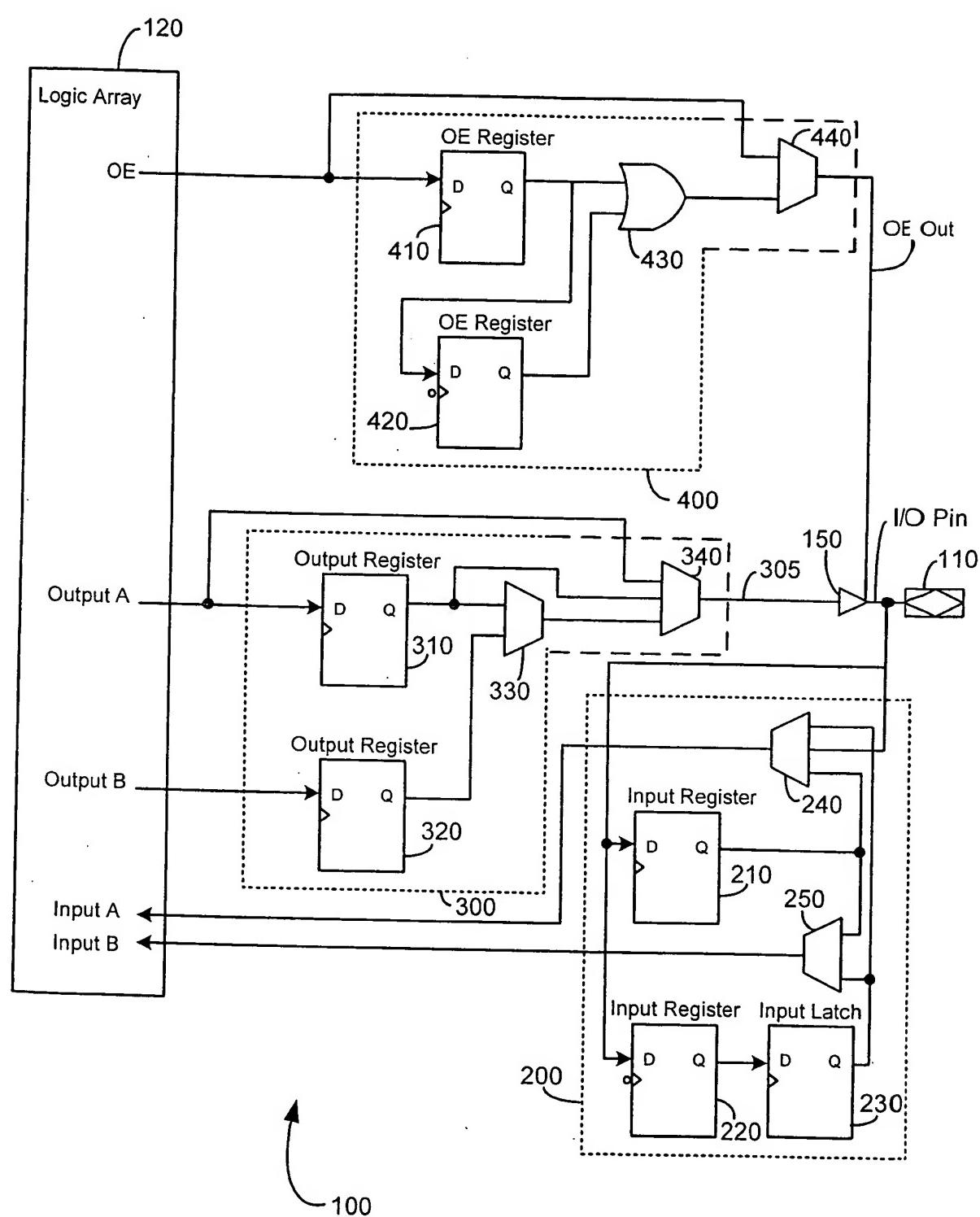


FIG. 1

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Logic Devices

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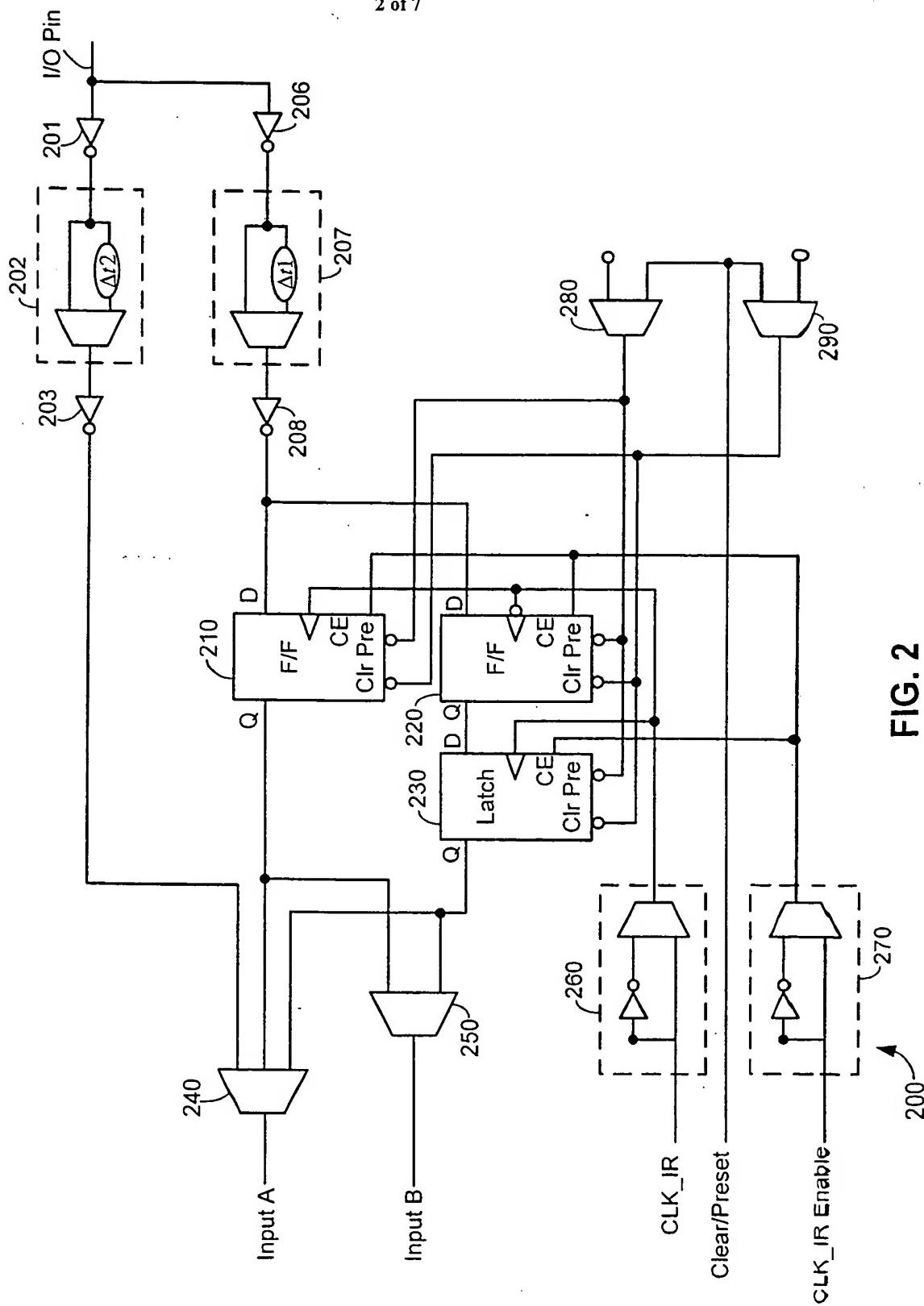


FIG. 2

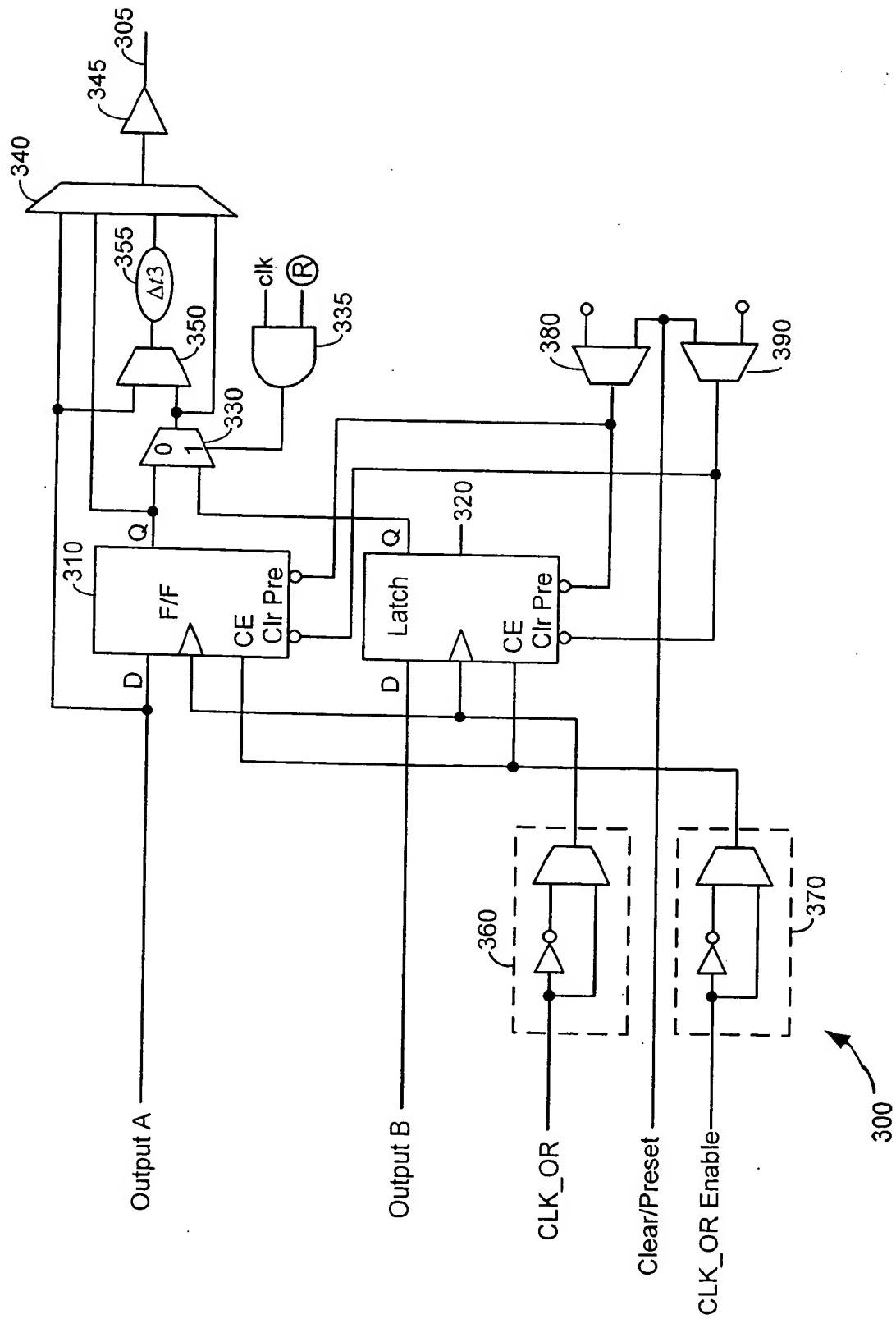


FIG. 3

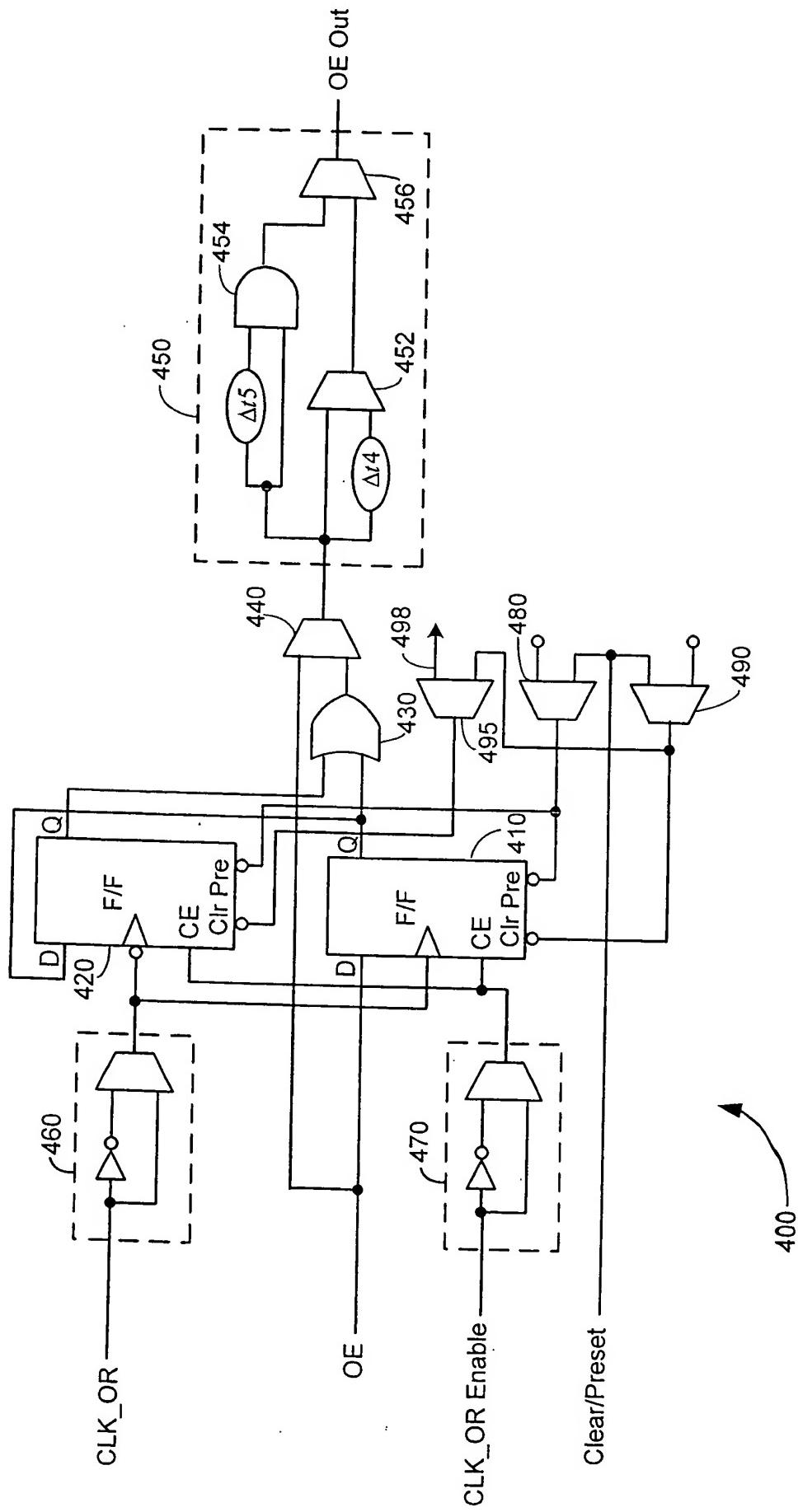


FIG. 4

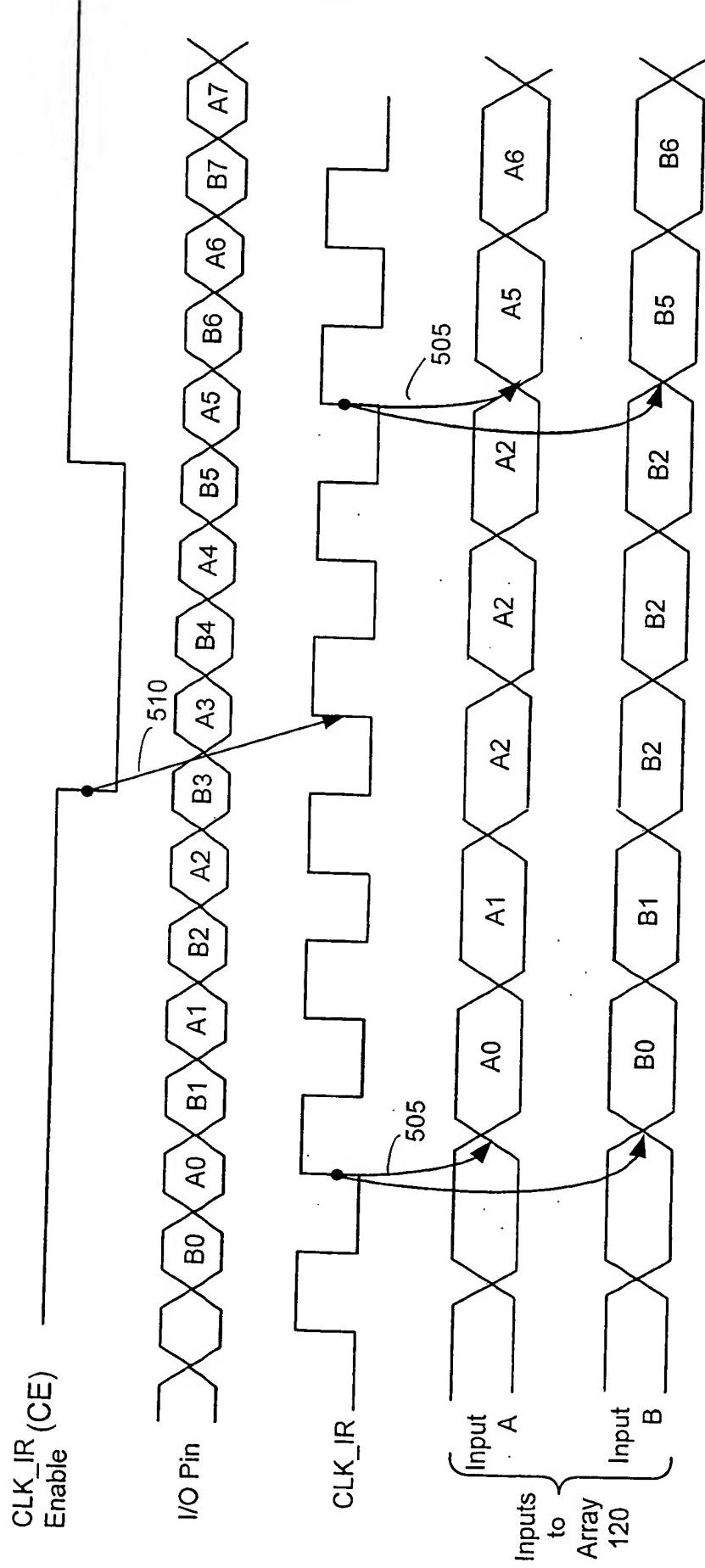


FIG. 5

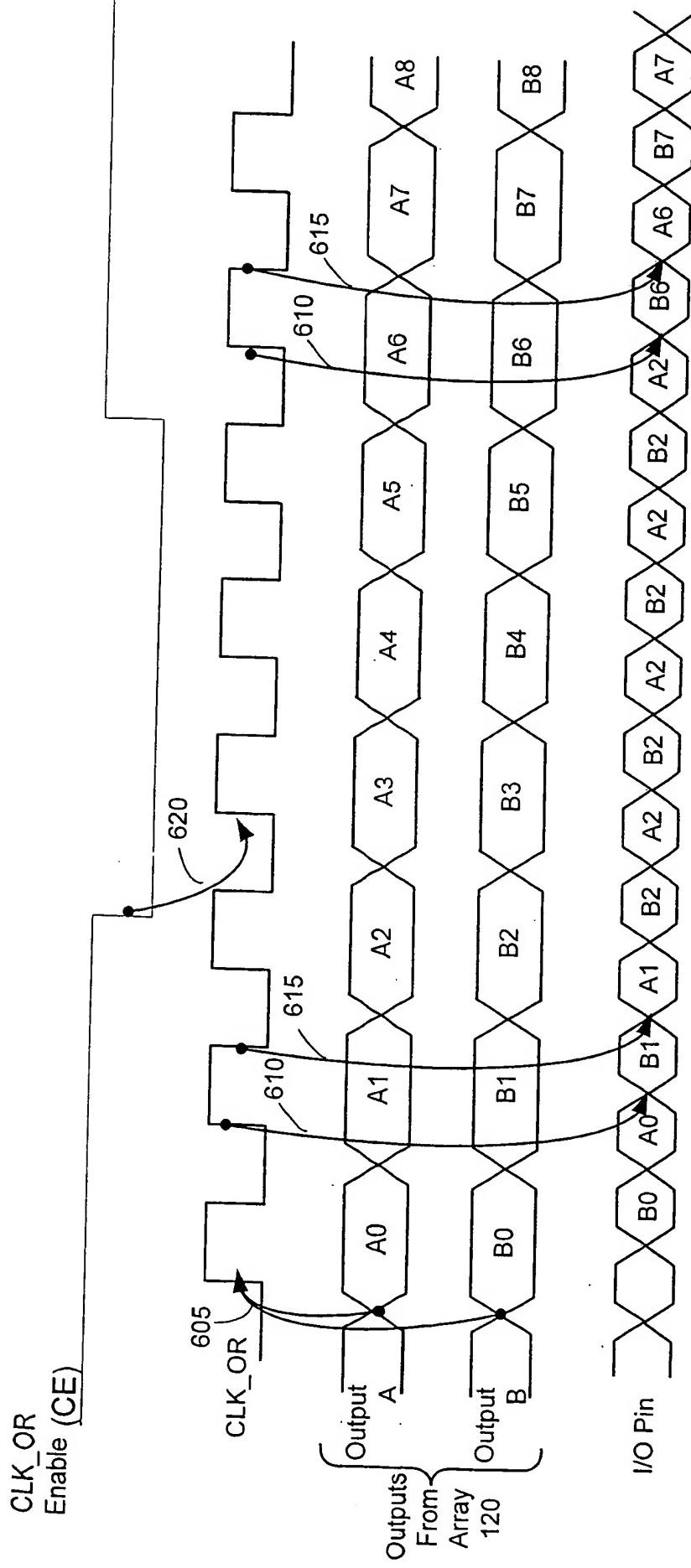


FIG. 6

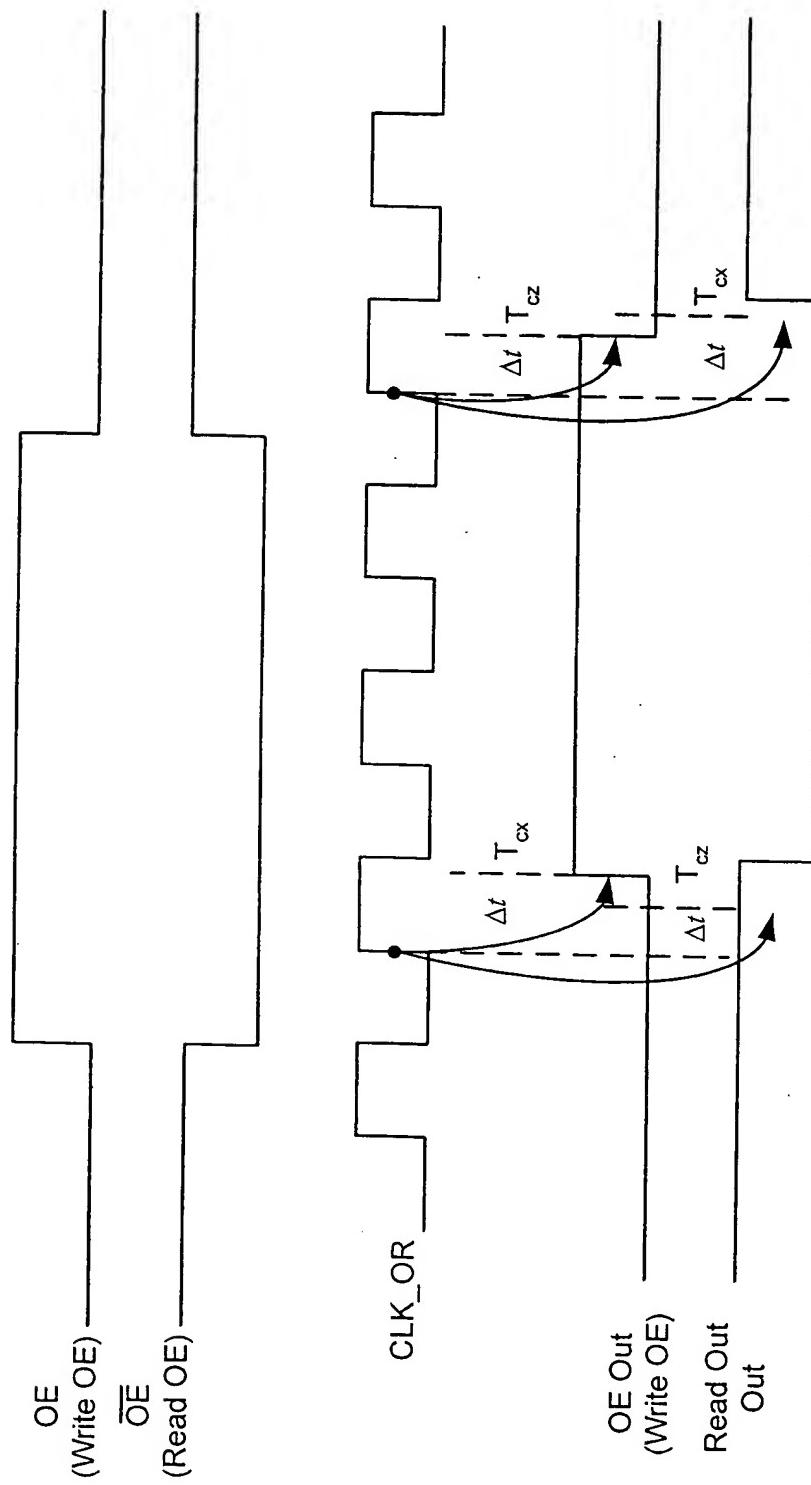


FIG. 7